

Fig. 1A

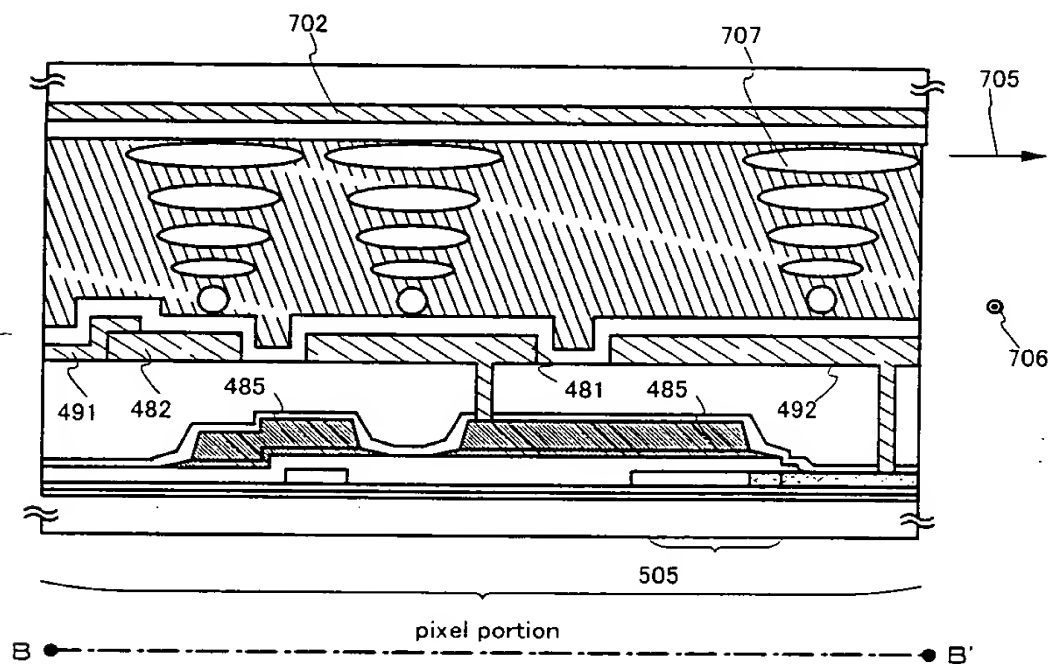


Fig. 1B

B — pixel portion — B'



FIG. 3

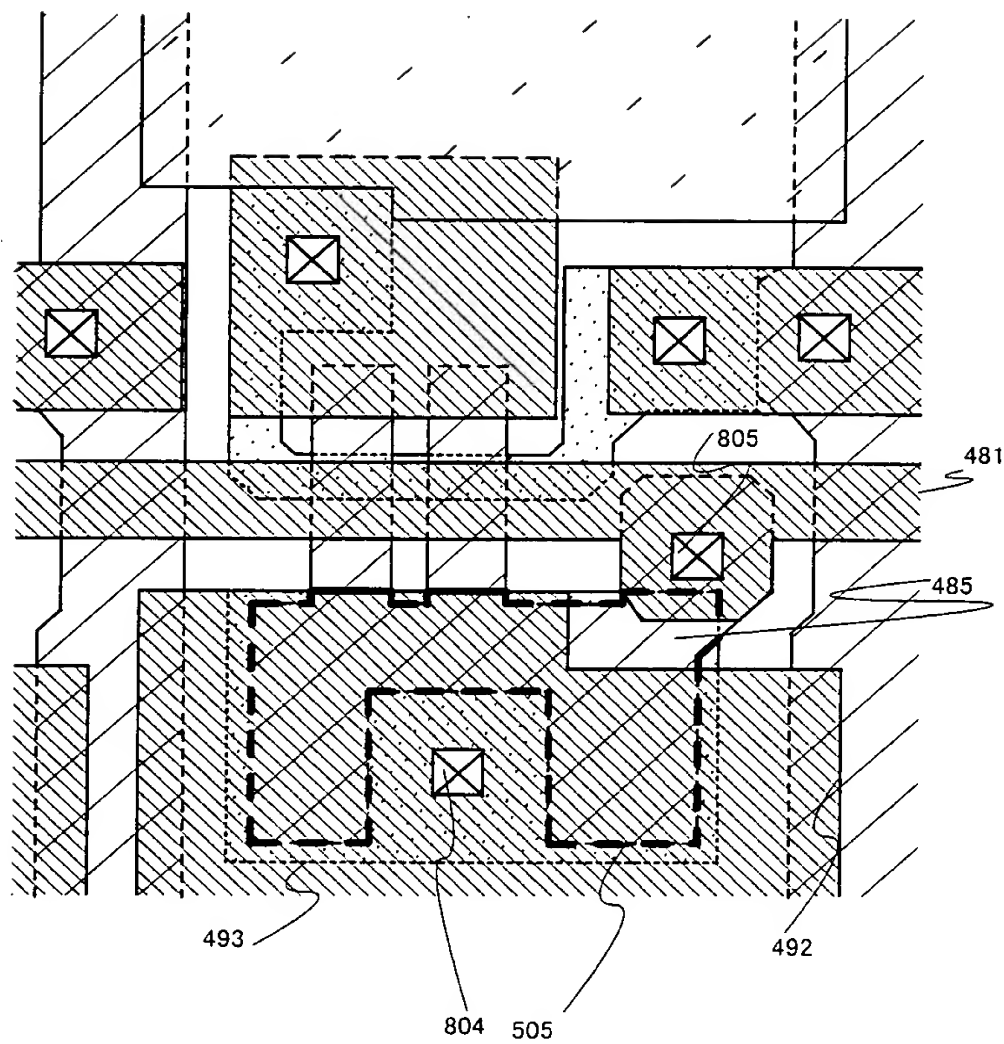


Fig. 3

FIG. 4

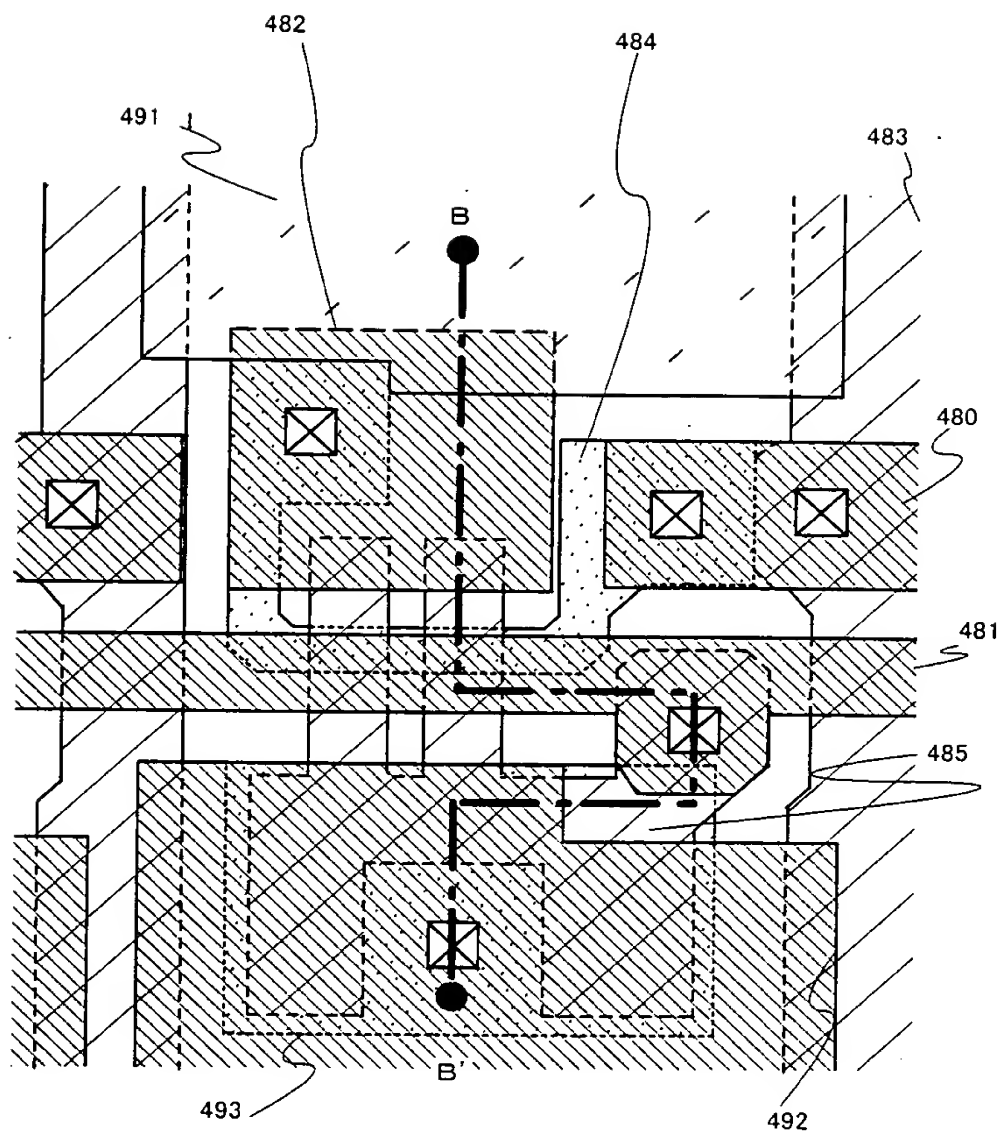


Fig. 4

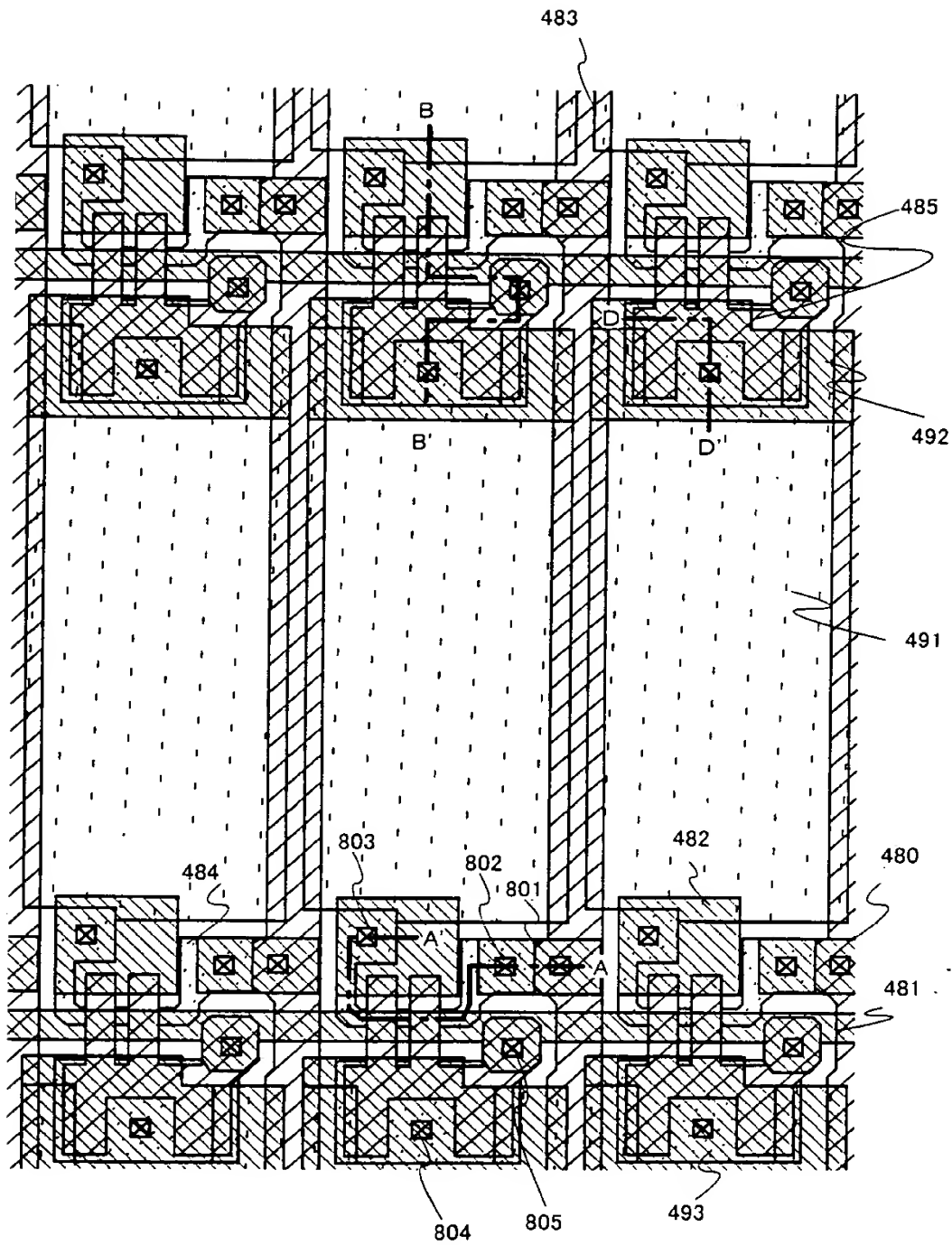


Fig. 5

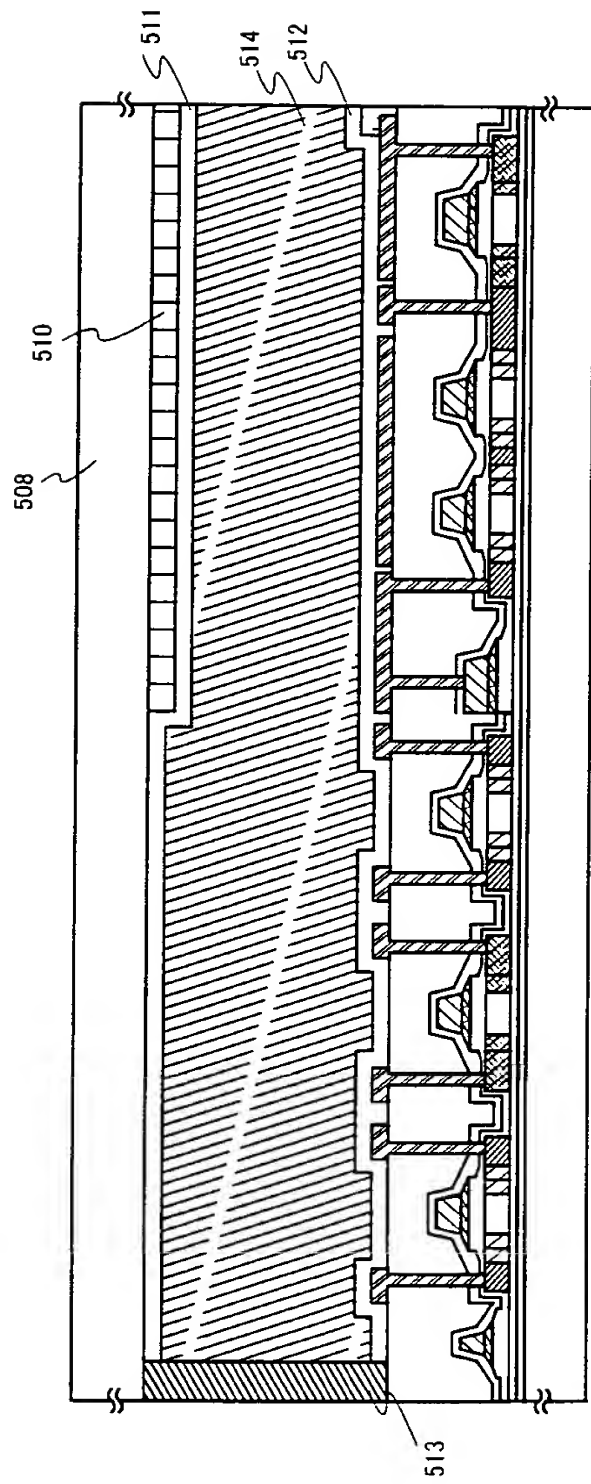


Fig. 6

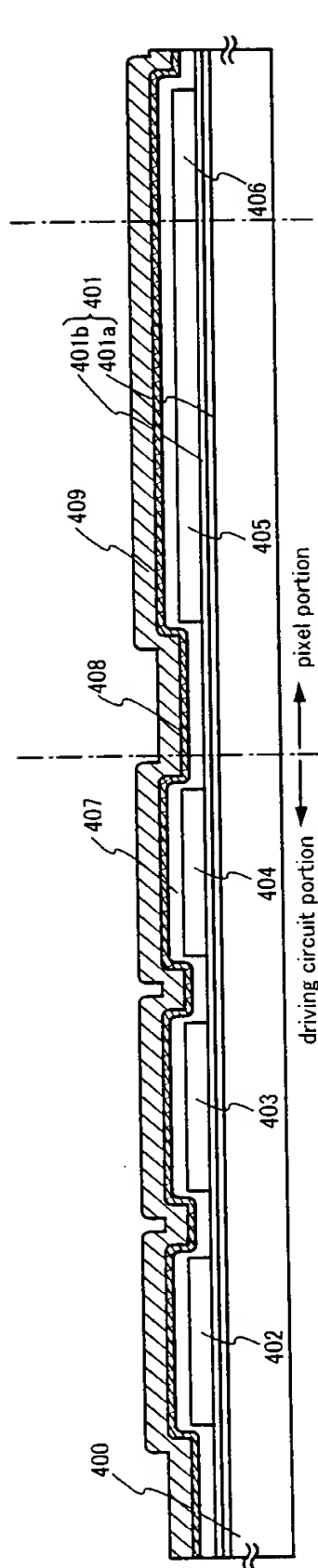


Fig. 7A

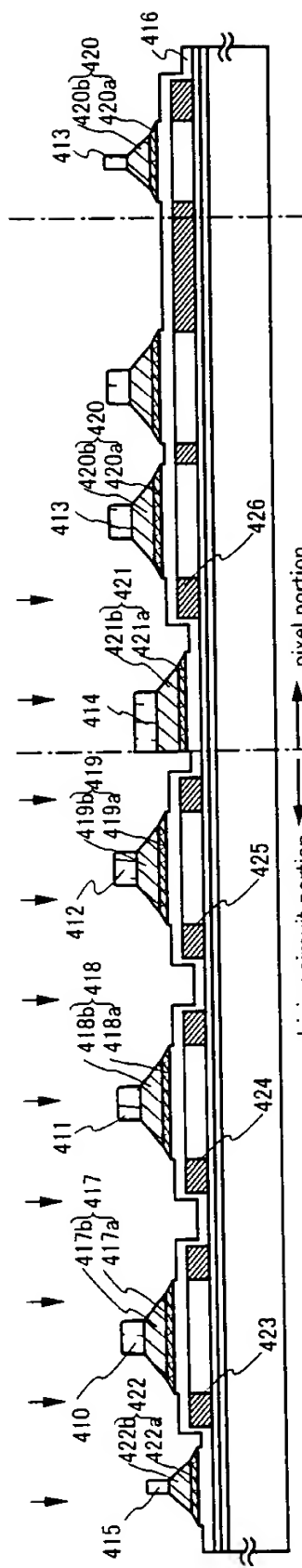


Fig. 7B

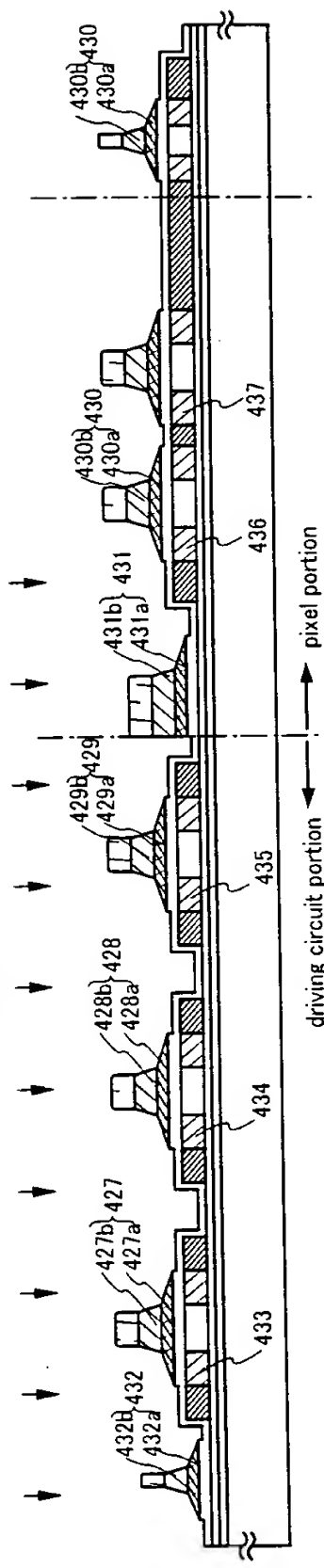


Fig. 7C

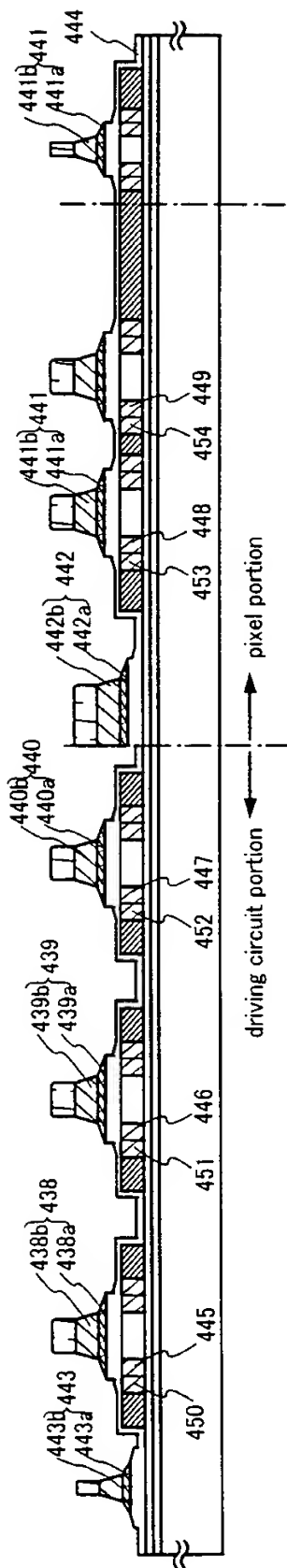


Fig. 8A

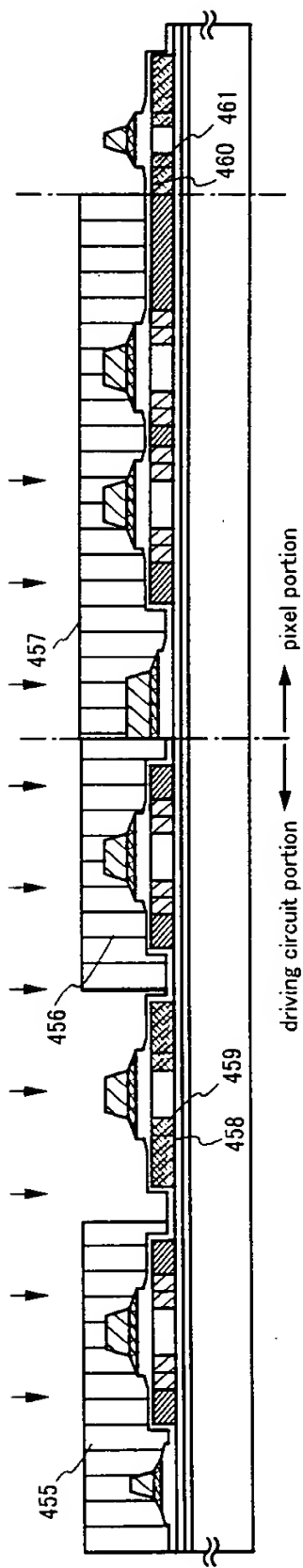


Fig. 8B

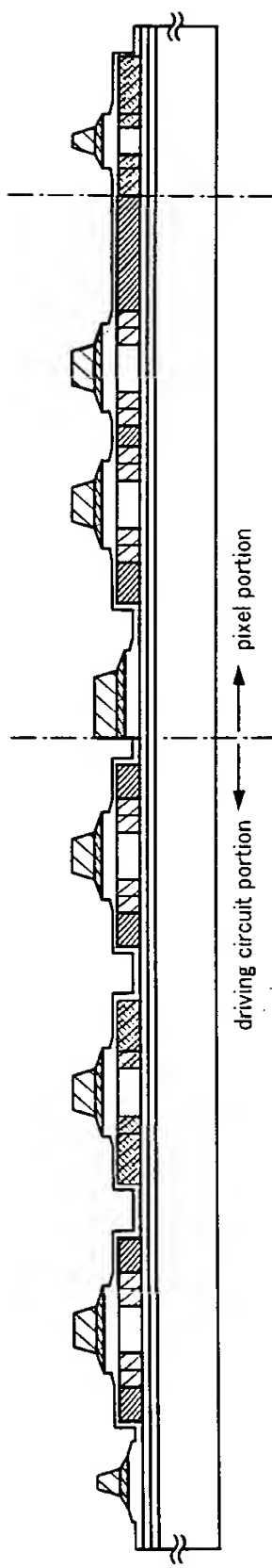


Fig. 8C



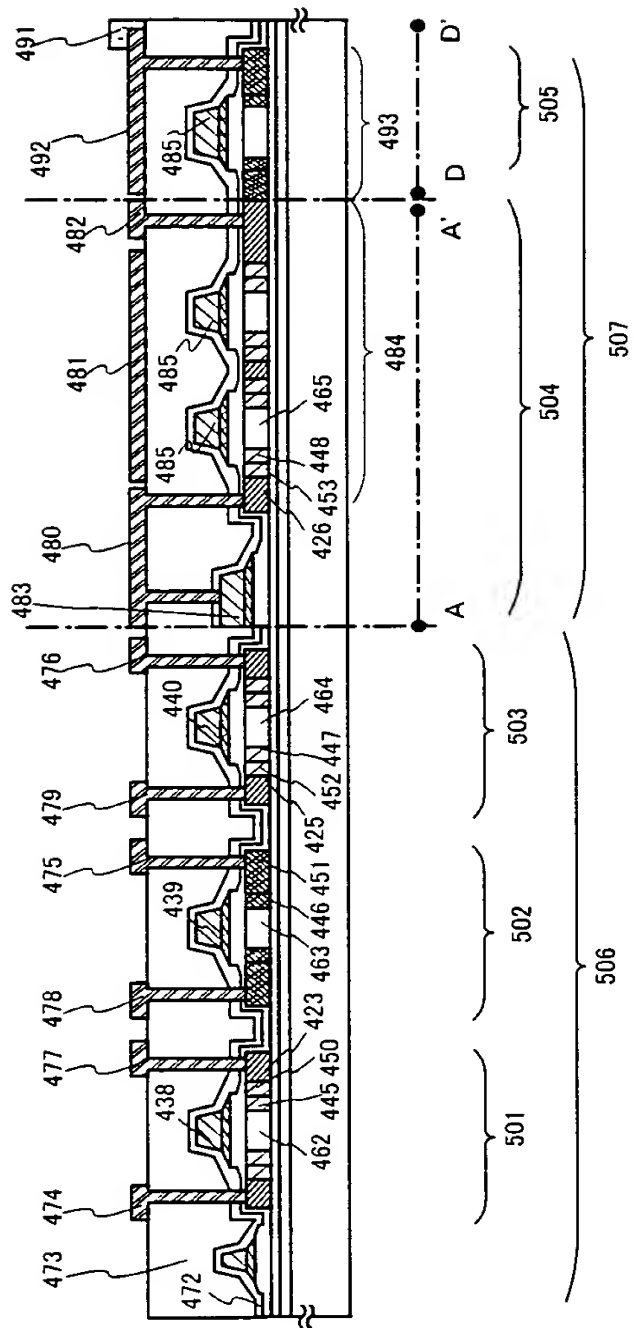


Fig. 9

Fig. 10A

Fig. 10B

Fig. 10C

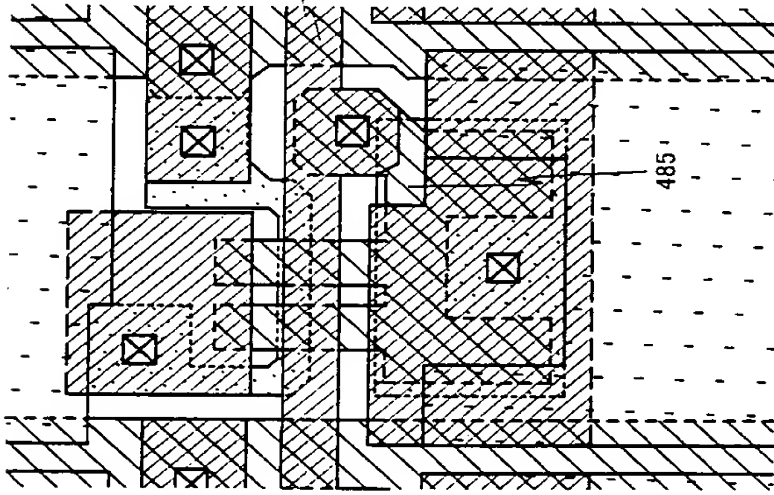


Fig. 11A

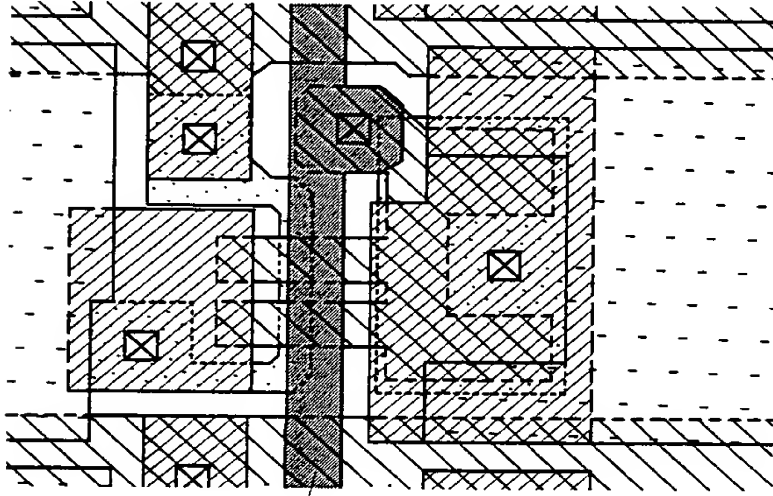


Fig. 11B

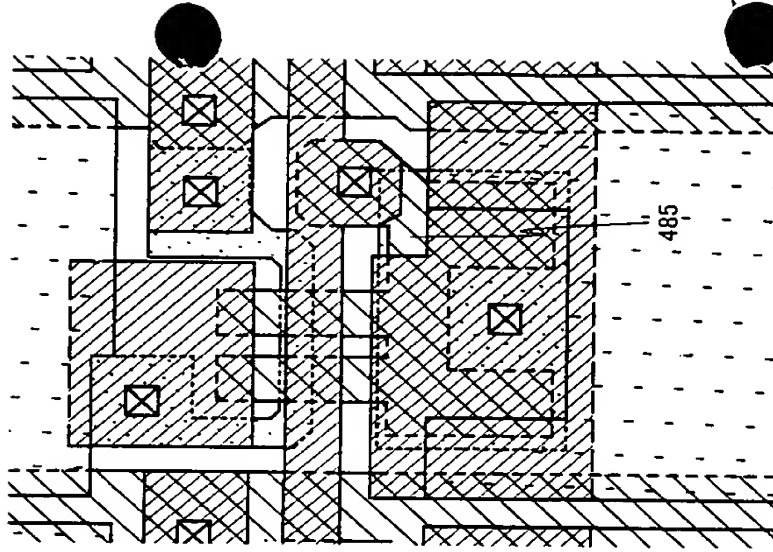


Fig. 11C

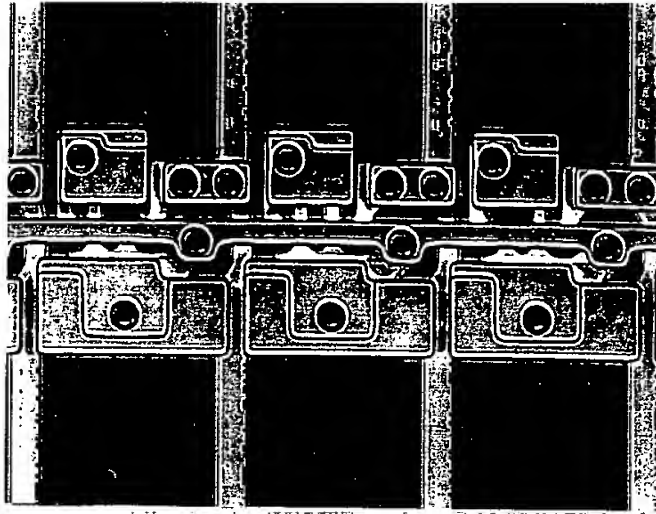


Fig. 12A

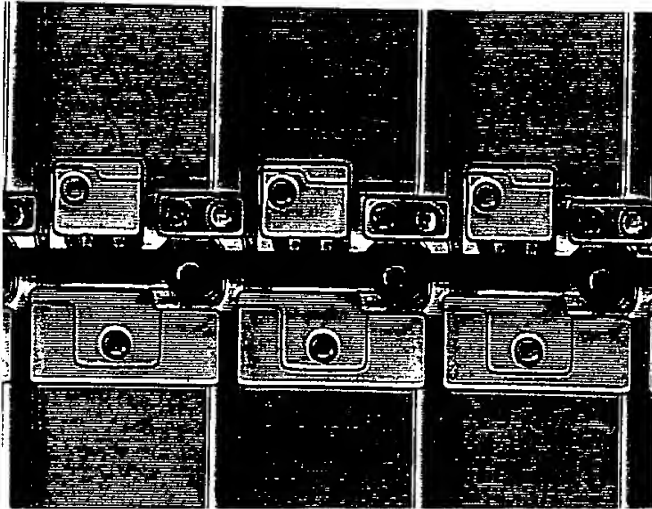


Fig. 12B

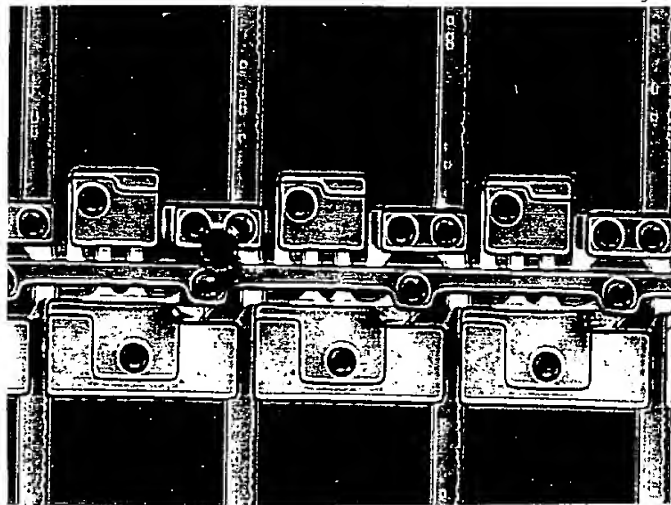


Fig. 12C

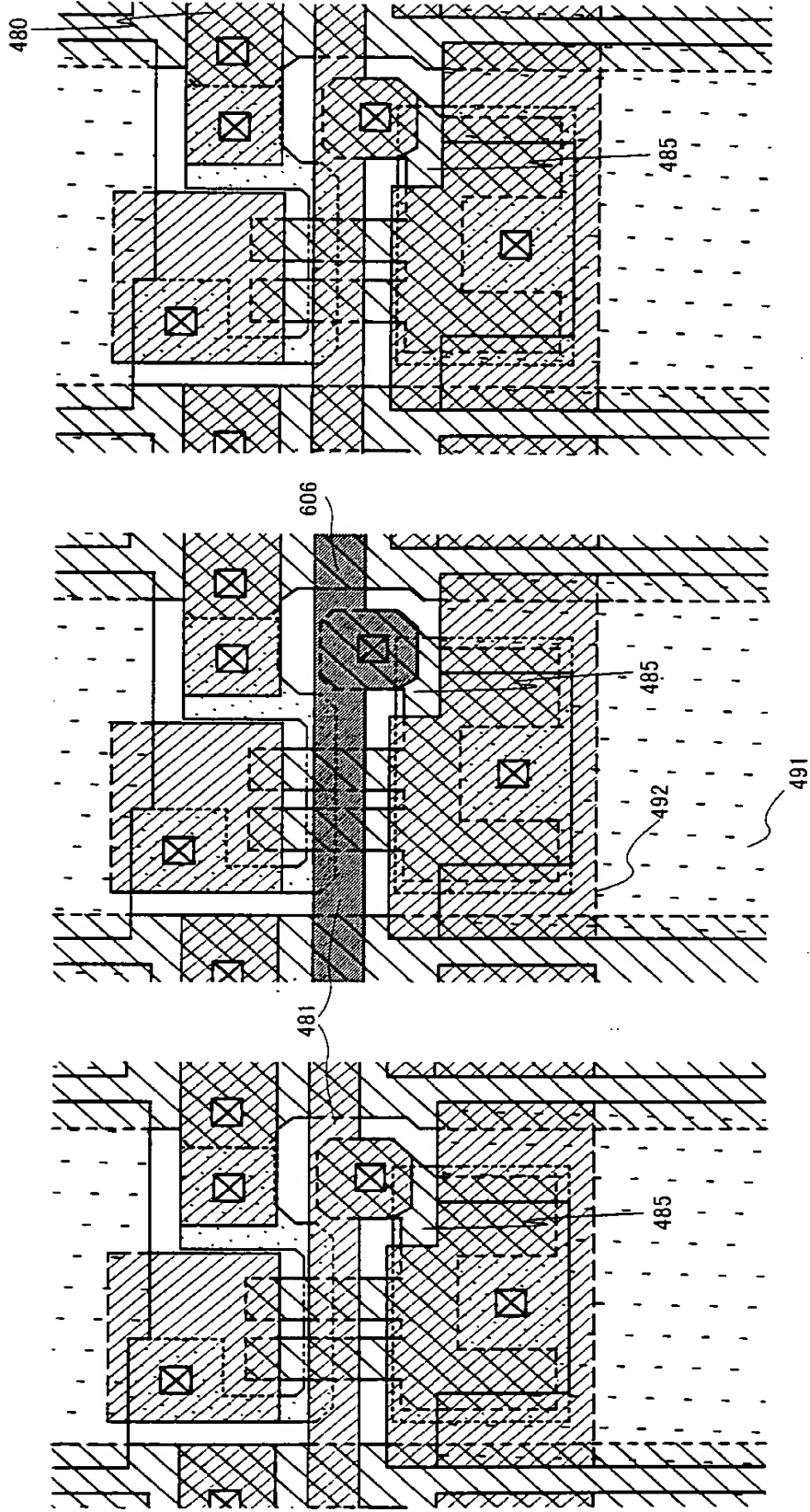


Fig. 13A

Fig. 13B

Fig. 13C

This cross-sectional view shows a central channel region (482) flanked by two vertical regions (483 and 484). The top and bottom of the device are defined by layers 480 and 481. Various electrical contacts and interconnects are shown, with labels 485, 486, 487, 488, 489, 490, 491, 492, 493, and 494 indicating specific components. A central vertical line is labeled 495.

Fig. 14

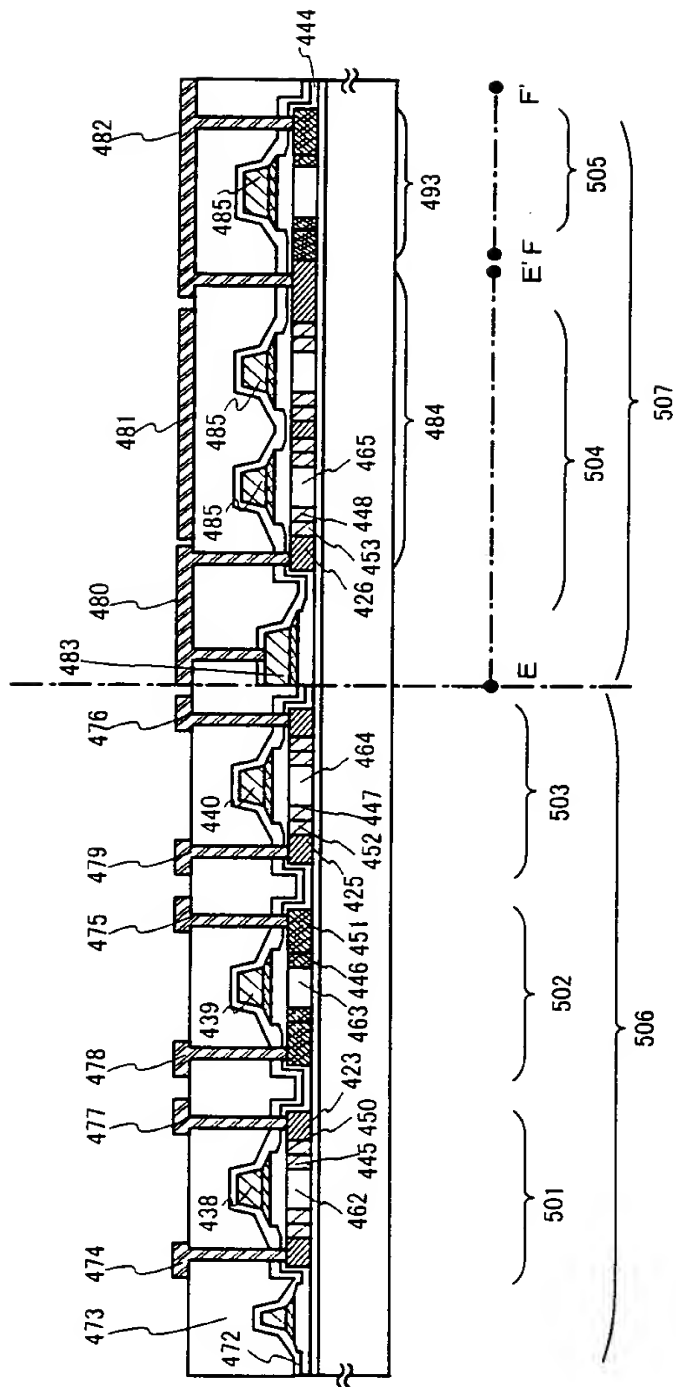


Fig. 15

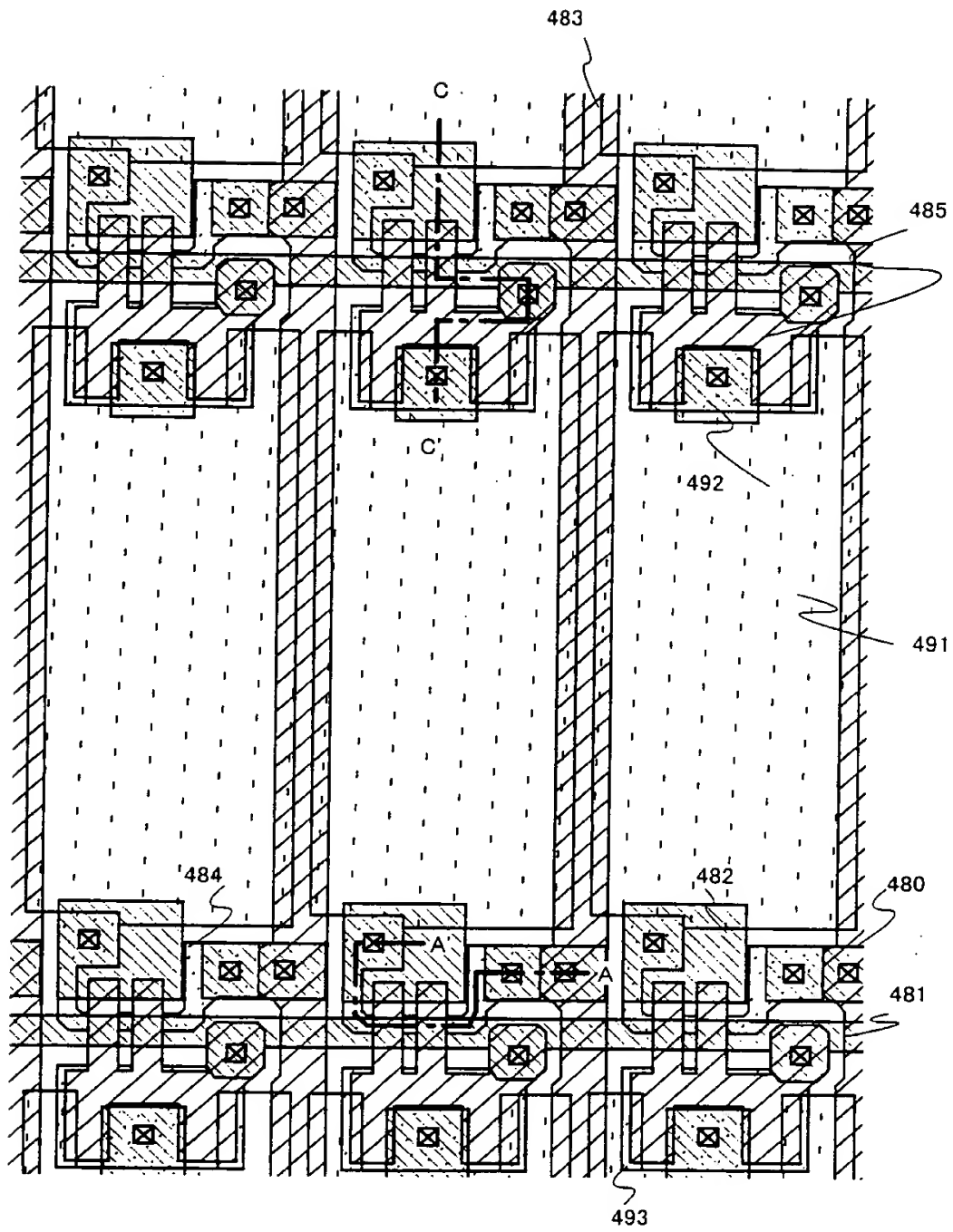


Fig. 16



FIG. 17

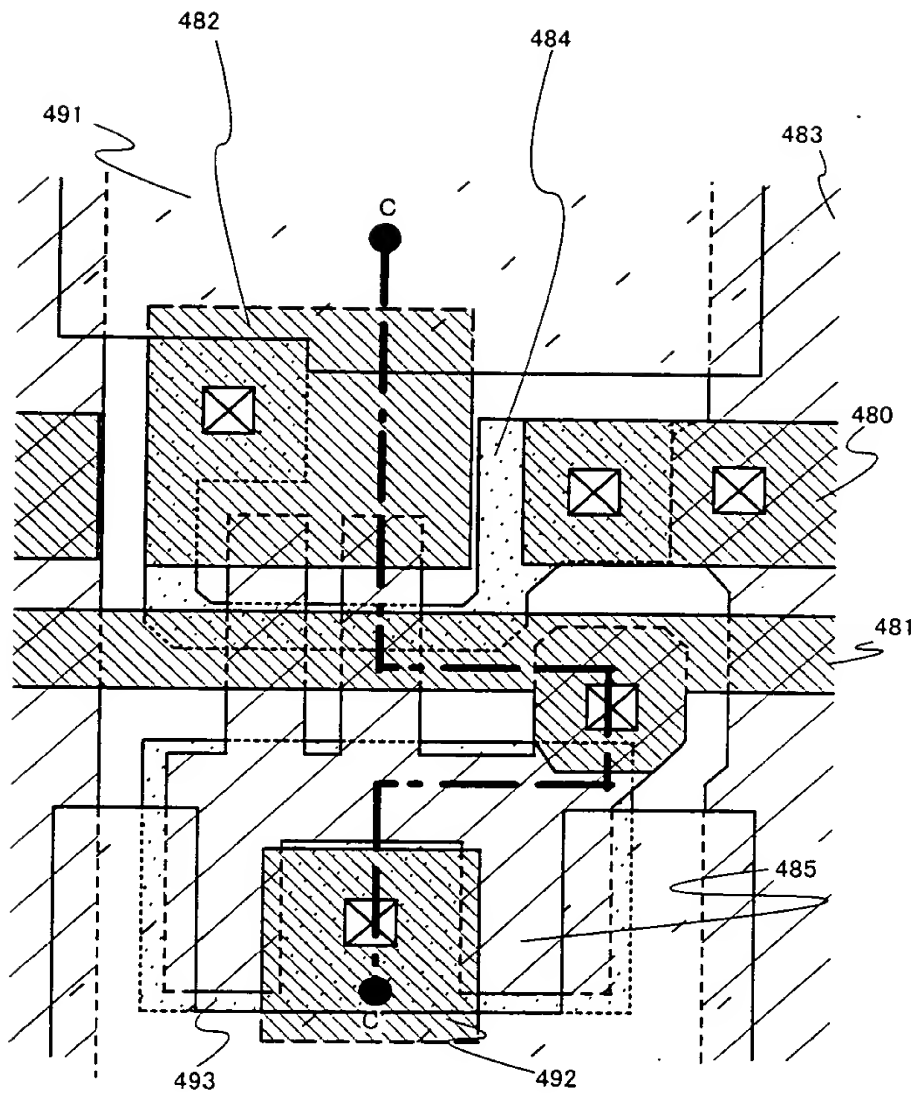


Fig. 17

0025486 .001001

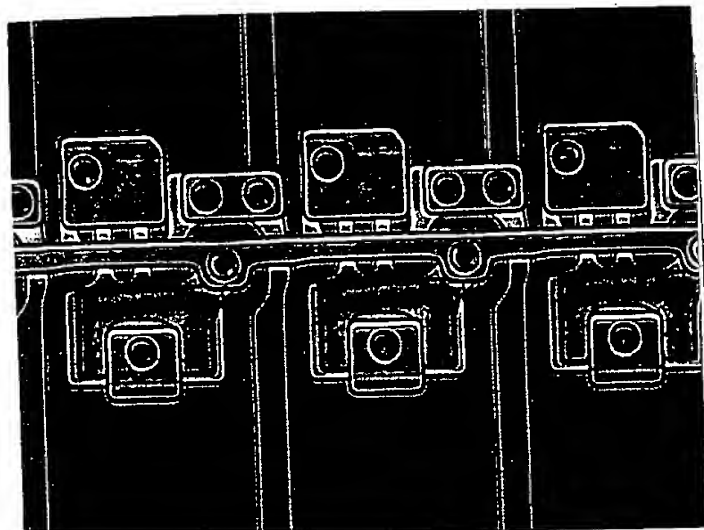


Fig. 18A

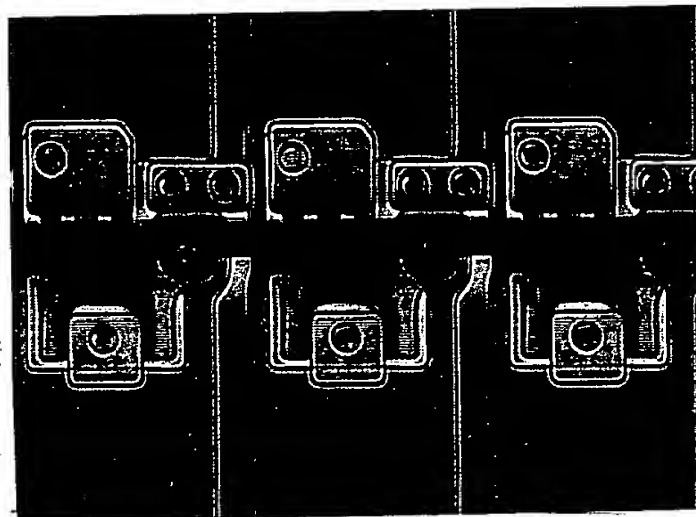


Fig. 18B

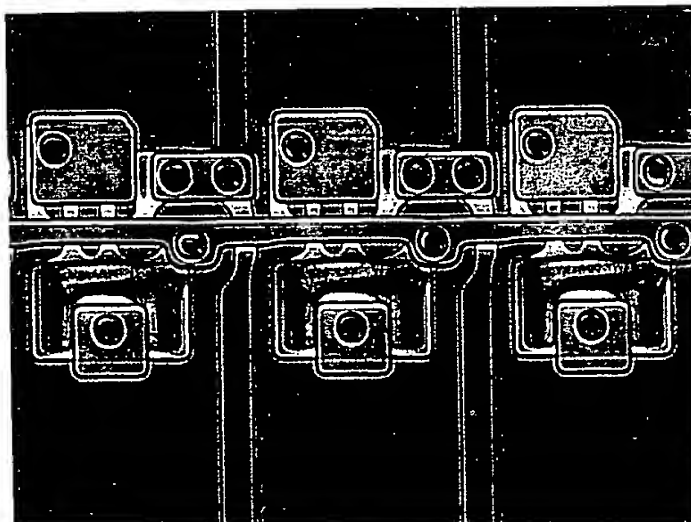


Fig. 18C

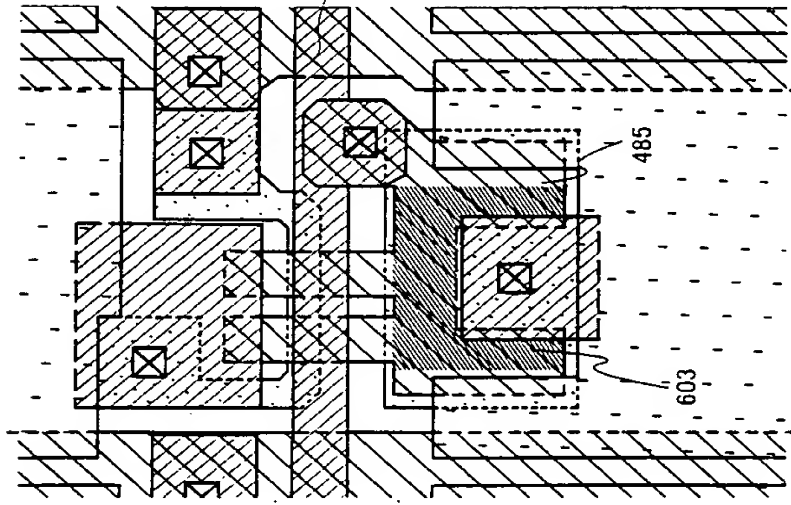


Fig. 19A

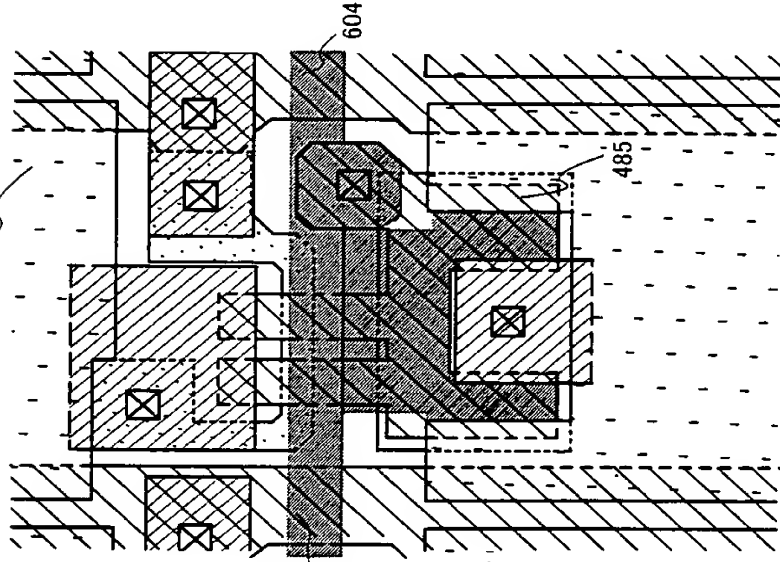


Fig. 19B

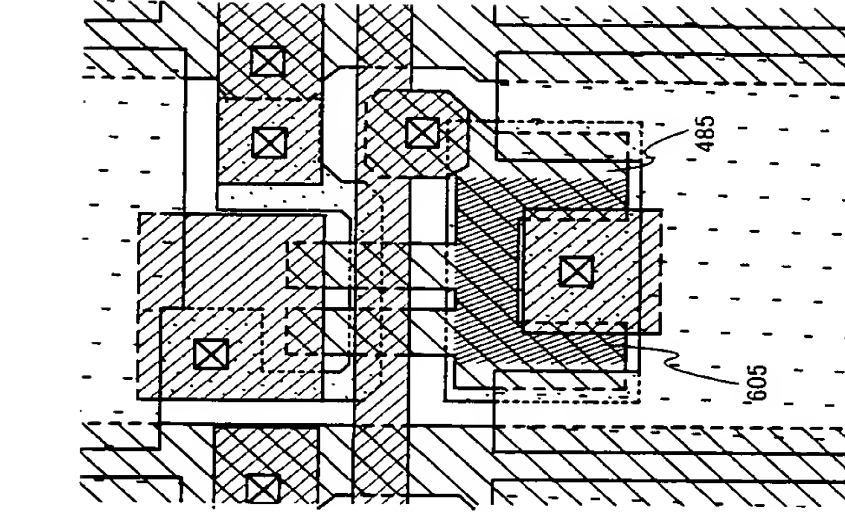


Fig. 19C

Fig. 20A

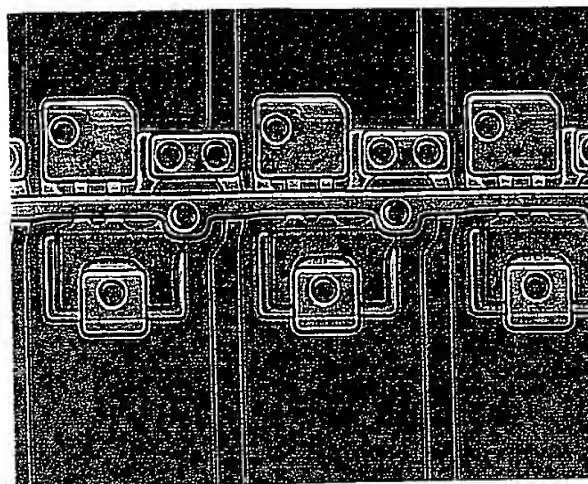


Fig. 20B

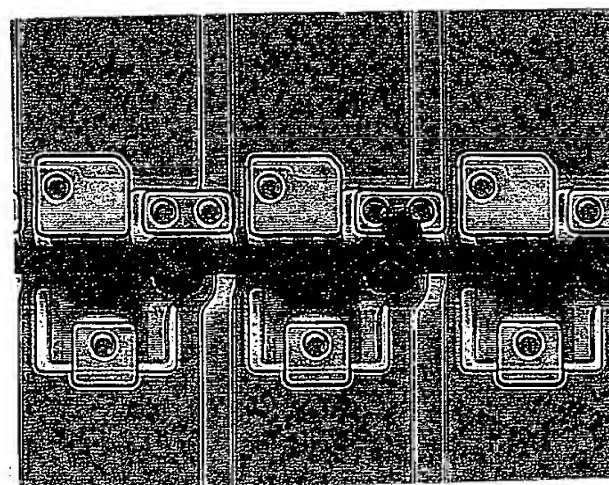
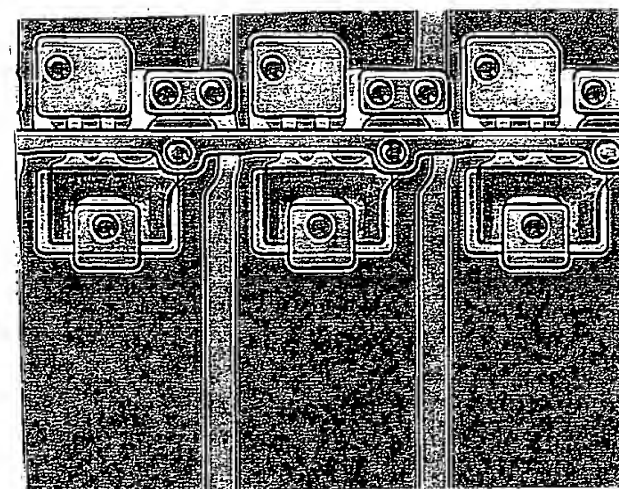
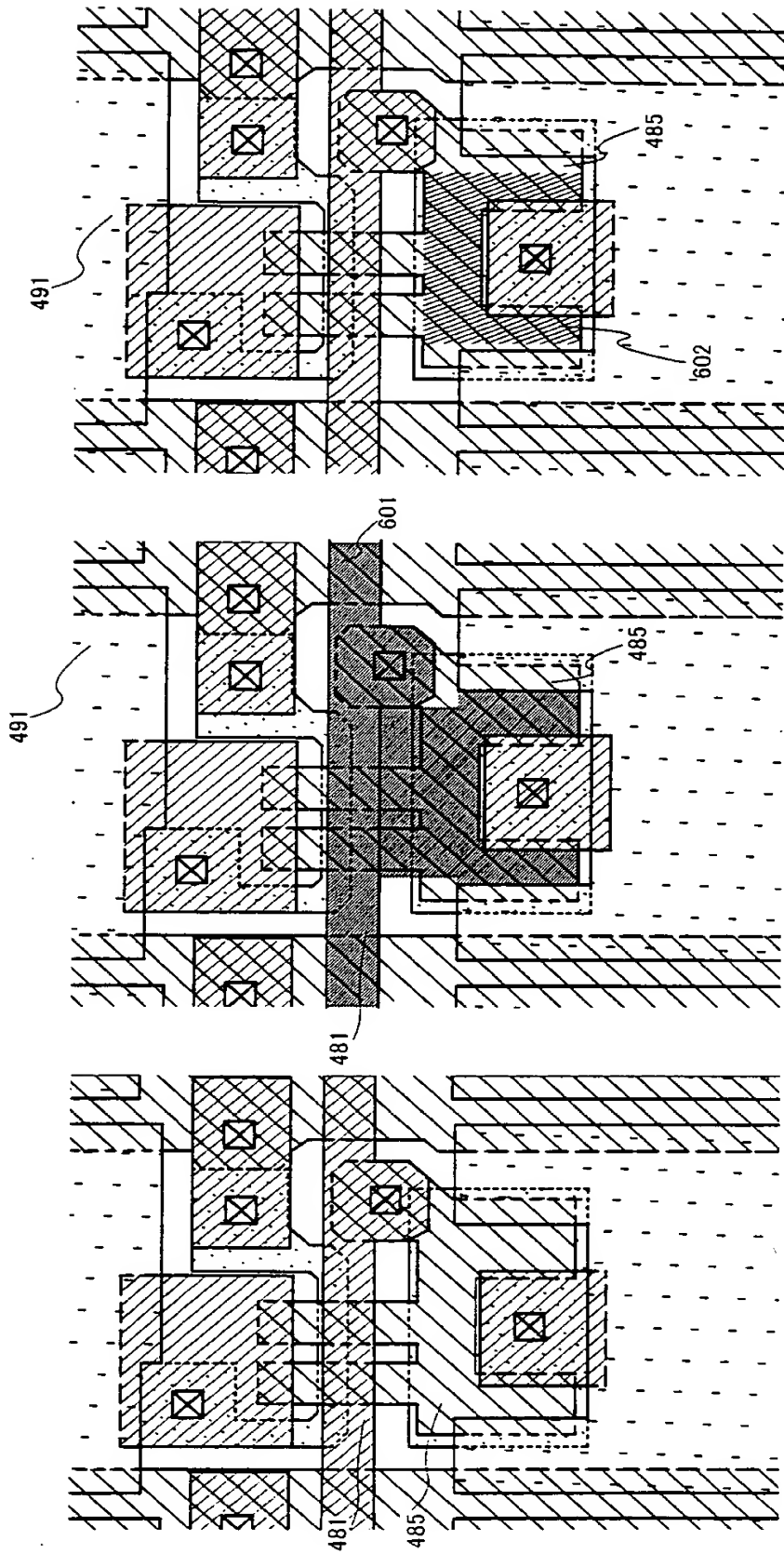


Fig. 20C





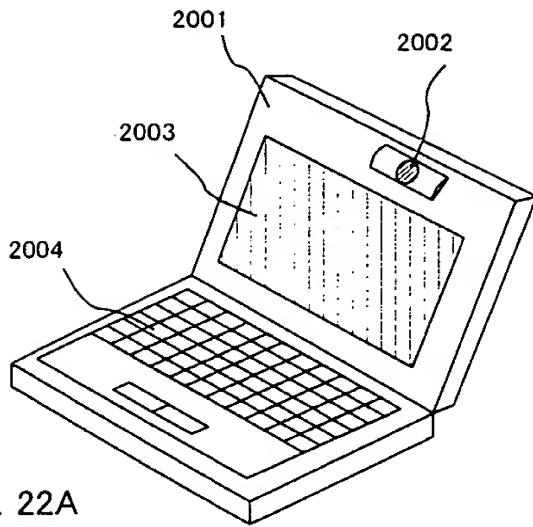


Fig. 22A

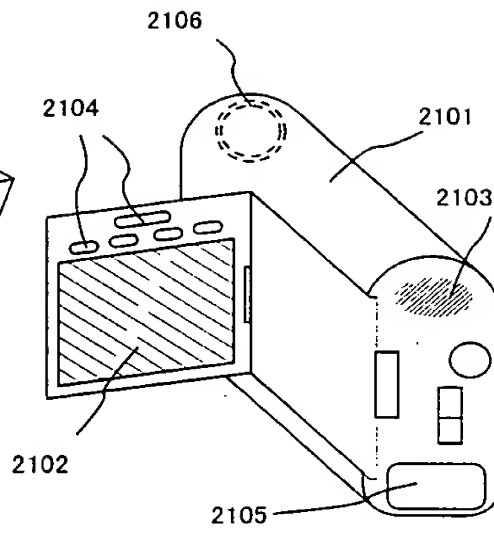


Fig. 22B

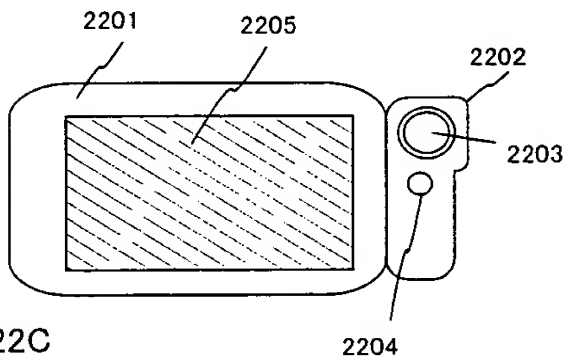


Fig. 22C

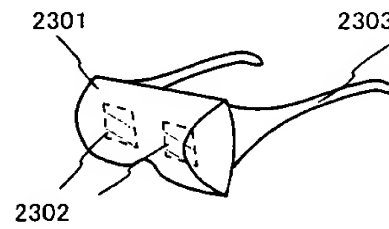


Fig. 22D

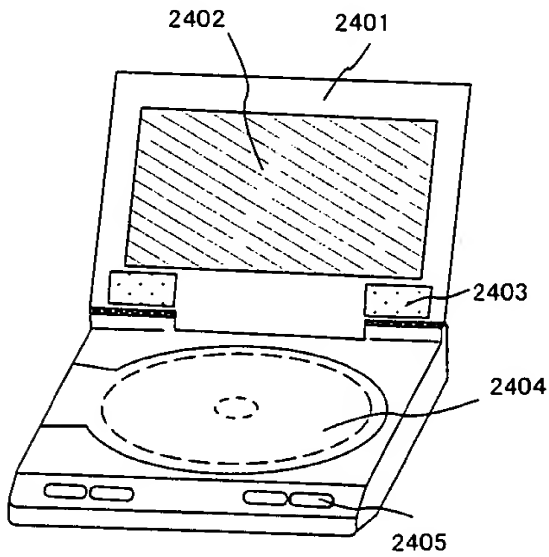


Fig. 22E

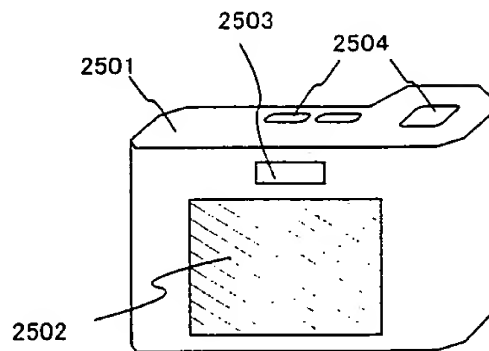


Fig. 22F

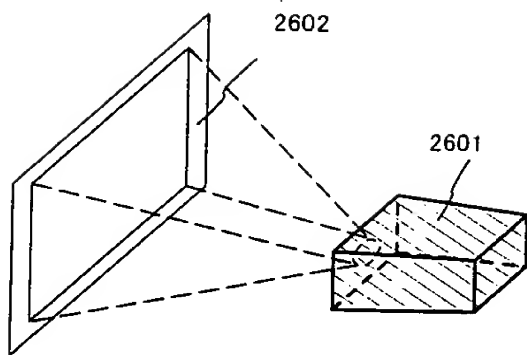


Fig. 23A

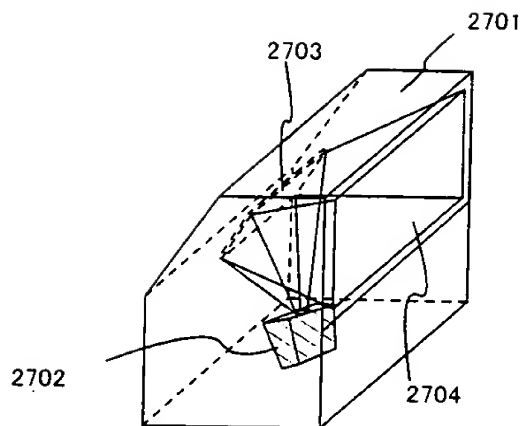


Fig. 23B

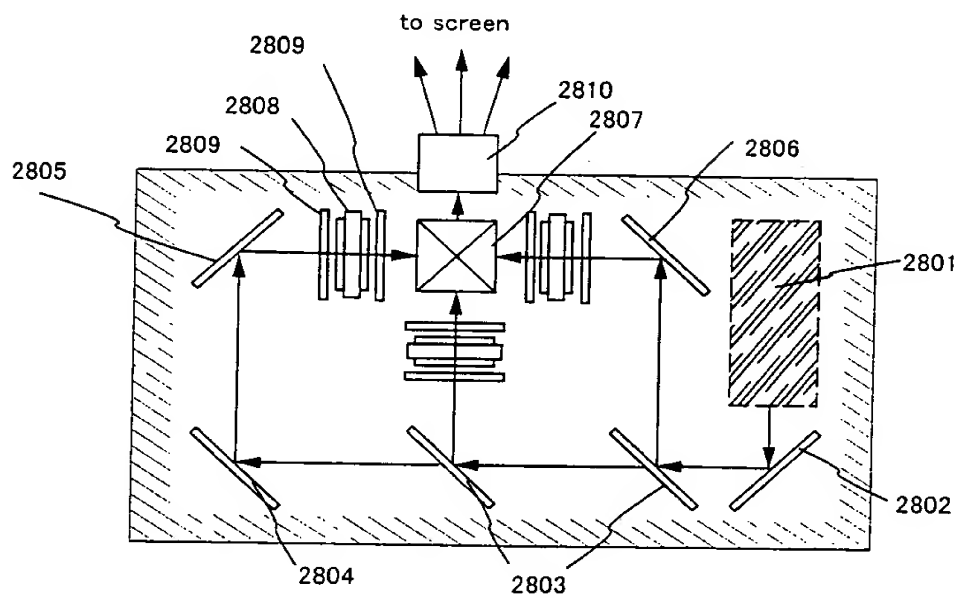


Fig. 23C

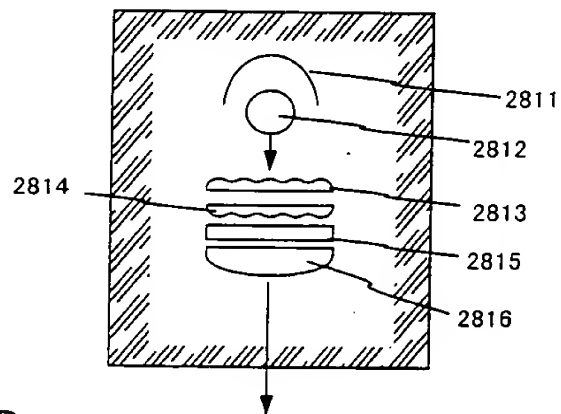


Fig. 23D

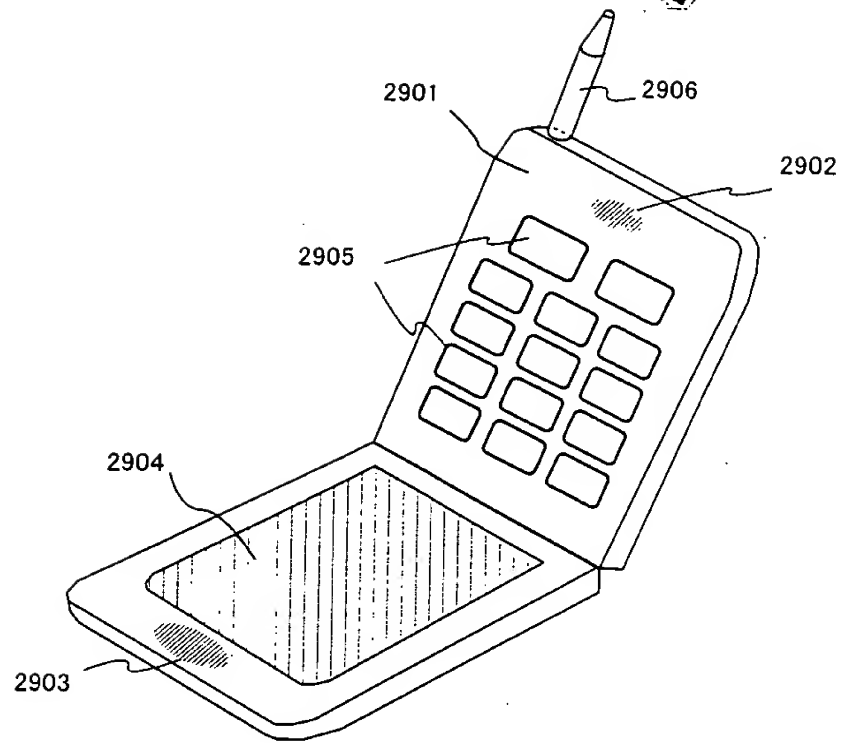


Fig. 24A

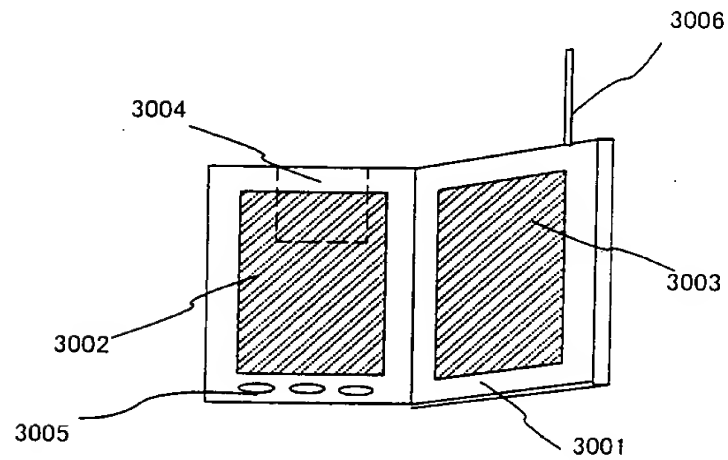


Fig. 24B

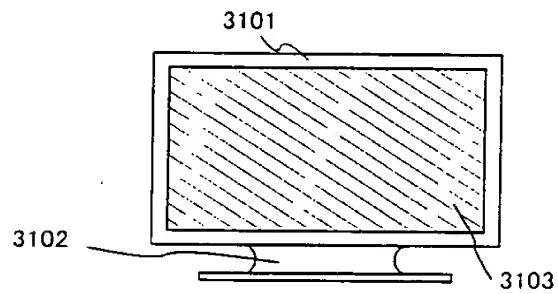


Fig. 24C